

FORM PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	ATTY. DOCKET NO. CH9/2002/0009US1	SERIAL NO. 10/658,311
	APPLICANT: ANDREAS HERKERSDORF, ET AL	
	FILING DATE: Herewith	GROUP:



REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
RS	AA	US2002/0091856	7/11/02	D.A. Brown	709/238	G06F15/173	10/31/01

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
RS	AB	EP 1,011 231	06/21/00	EP	H04L /12/56	H04L29/06		
RS	AC	EP 1,122,927	08/08/01	EP	H04L/29/06	H04L12/56		

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

RS	AD	IP Caching For Terabit Speed Routers, B. Talbot, T. Sherwood, B. Lin, GlobeCom'99, 1999 IEEE, pp. 1565-1569						
AS	AE	Performance Evaluation Of Hierarchical Caching In High-Speed Routers, E. Besson, P. Brown, Globe Com'98, 1998 IEEE, pp. 2640-2645						
RS	AF	Improving Gateway Performance With A Routing-Table Cache, David C. Feldmeier, InfoCom'88, 1988 IEEE, pp. 298-307						
RS	AG	High-Performance IP Routing Table Lookup Using CPU Caching, Tzi-cker Chiueh, Prashant Pradhan, InfoCom, 1999, IEEE , pp. 1421-1428						
RS	AH	Cache Memory Design For Internet Processors, Tzi-cker Chiueh, Prashant Pradhan, InfoCom 2000 IEEE, pp. 2-7						
LS	AI	Router Plugins: A Software Architecture for Next-Generation Routers, Dan DeCasper, Zubin Dittia, Guru Prulkar and Bernhard Plattner, IEEE/ACM Transactions On Networking, Vol. 8, No. 1, February 2000, pp. 135-140						
RS	AJ	A Novel Cache Architecture to Support Layer-Four Packet Classification at Memory Access Speeds, Jun Xu, Mukesh Singhal, Joanne Degroat, IEEE, InfoCom, 2000, pp. 1445-1454						

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.